Phase locked loops: design, simulation, and applications.

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Book

Title	**Phase locked loops : design, simulation, and applications**
Edition	6th ed.
Author(s)	Best, Roland E
Publication	[S.l.] : McGraw-Hill Professional, 2007. - 490 p.
Note	The book can be consulted by contacting: TE-EPC-CCS : Magrans De Abril, Marc
Subject category	xx
Abstract	The Definitive Introduction to Phase-Locked Loops, Complete with Software for Designing Wireless Circuits! The Sixth Edition of Roland Best's classic Phase-Locked Loops has been updated to equip you with today's definitive introduction to PLL design, complete with powerful PLL design and simulation software written by the author. Filled with all the latest PLL advances, this celebrated sourcebook now includes new chapters on frequency synthesis...CAD for PLLs...mixed-signal PLLs...all-digital PLLs...and software PLLs_plus a new collection of sample communications applications. An essential tool for achieving cutting-edge PLL design, the Sixth Edition of Phase-Locked Loops features: A wealth of easy-to-use methods for designing phase-locked loops Over 200 detailed illustrations New to this edition: new chapters on frequency synthesis, including fractional-N PLL frequency synthesizers using sigma-delta modulators; CAD for PLLs, mixed-signal PLLs, all-digital PLLs, and software PLLs; new PLL communications applications, including an overview on digital modulation techniques Inside this Updated PLL Design Guide • Introduction to PLLs • Mixed-Signal PLL Components •

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