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Title

CMOS analog circuit design

Author(s)

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Publication

New York, NY : Oxford University Press, 1987. - 701 p.

Series

[\(The Oxford series in electrical and computer engineering\)](#)Subject
code[621.3.049](#)Subject
category

Engineering

Abstract

This text presents the principles and techniques for designing analog circuits to be implemented in a CMOS technology. The level is appropriate for seniors and graduate students familiar with basic electronics, including biasing, modeling, circuit analysis, and some familiarity with frequency response. Students learn the methodology of analog integrated circuit design through a hierarchically-oriented approach to the subject that provides thorough background and practical guidance for designing CMOS analog circuits, including modeling, simulation, and testing. The authors' vast industrial experience and knowledge is reflected in the circuits, techniques, and principles presented. They even identify the many common pitfalls that lie in the path of the beginning designer--expert advice from veteran designers. The text mixes the academic and practical viewpoints in a treatment that is neither superficial nor overly detailed, providing the perfect balance.

ISBN

0195107209 (This book at [Amazon](#)) (print version, hardback)Other
editions[2nd ed. \(2002\)](#)[3rd ed. \(2012\)](#)

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